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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/043,204	01/14/2002	Takeshi Nakao	36856.593	36856.593 7389	
75	90 07/29/2004		EXAMINER		
Keating & Bennett LLP			BUDD, MARK OSBORNE		
Suite 312 10400 Eaton Pla	ace		ART UNIT	PAPER NUMBER	
Fairfax, VA 22030			2834		
			DATE MAIL ED: 07/20/200	DATE MAIL ED: 07/20/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/043,204	NAKAO ET AL.					
Office Action Summary	Examiner	Art Unit	_				
	Mark Budd	2834					
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address					
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
Status							
1)⊠ Responsive to communication(s) filed on 09 Ju	ine 2004						
	action is non-final.						
3) Since this application is in condition for allowar		secution as to the merits is					
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4)⊠ Claim(s) <u>1,3-5,7 and 9-11</u> is/are pending in the	application						
4a) Of the above claim(s) is/are withdraw	• •						
5) Claim(s) is/are allowed.	The state of the s						
6)⊠ Claim(s) <u>1,3-5,7 and 9-11</u> is/are rejected.	_						
7) Claim(s) is/are objected to.							
•	Claim(s) are subject to restriction and/or election requirement.						
Application Papers							
9) The specification is objected to by the Examine	r						
9)☐ The specification is objected to by the Examiner. 0)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
Applicant may not request that any objection to the							
Replacement drawing sheet(s) including the correcti							
11) The oath or declaration is objected to by the Ex		· · ·					
Priority under 35 U.S.C. § 119							
12) ☐ Acknowledgment is made of a claim for foreign	priority under 25 H.C.C. \$ 440(a)	\ (d\ an (6)					
a) ☐ All b) ☐ Some * c) ☐ None of:	priority under 35 U.S.C. 9 119(a)	-(a) or (t).					
1. Certified copies of the priority documents	s have been received						
2. Certified copies of the priority documents		on No					
3. Copies of the certified copies of the prior							
application from the International Bureau		d III tilis National Stage					
* See the attached detailed Office action for a list of	* **	d.					
	•						
Attachment(s)							
1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO_413)					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	ite					
Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 5-8-04	5) Notice of Informal P 6) Other:	atent Application (PTO-152)					
							

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Claims 1, 3-5, 7 and 9-11 are rejected und4er 35 USC 103 as unpatentable over the Prior Art (Applicants fig. 4) (Referenced by Applicant as AAPA) in view of Koshino.

The Prior art (applicant's fig. 4) teaches the specific SAW configurations but he jumper wires cross the electrodes and/or reflector elements. However Koshino (Figs. 6b, 10, 14, 16-18, 20, 21, 23 & 25) teach connection of multiple SAWS to the terminal pads via jumper wires that are specifically routed so as not to cross over any electrode or reflector fingers. Such constructions would eliminate possible unwanted capacitive coupling and possible short-circuiting during manufacturing.

Applicant has argued that Koshino does not teach how to avoid jumper wires from crossing the IDT's or reflectors of more than a single device. The examiner feels that applicant takes an unreasonably narrow view of Koshiro's teachings. Why would one deliberately allow jumper wires to crossover IDT's or reflectors just because more than one device is provided on the same substrate?

Additionally, applicant argues that the main thrust of Koshino's teaching is the even spacing of terminals and not avoiding jumper wires crossing over the active areas. The examiner notes fig. 23, where jumper wires #76 and #84 would clearly have a more direct path to their respective equally spaced terminals if these wires did cross over IDT #73 or reflector #75. Instead, a deliberately circuits route is shown. Thus the equal spacing of the terminals is not the reason for the jumper wires not crossing over an IDT or reflector. Japan (390) is withdrawn as a reference. The examiner is curios as to why applicants are requesting an English language translation of a document issued in their native tonque.

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Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Mark Budd whose telephone number is (703)308-3929.

The examiner can normally be reached on (571)272-2019 from 6am to 4pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, D. Schuberg, can be reached on (571)272-2044. The fax phone number for

the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

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Business Center (EBC) at 866-217-9197 (toll-free).

Budd/ds

07/22/04

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